



**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**ACADEMIC YEAR 2018-19 EVEN**

Report for

**ONE DAY WORKSHOP ON**  
**“Microelectronics and VLSI design using advanced simulation tools”**

**Prepared by**  
**Mrs.P.Geethabai, AP/ECE**  
**Mr. R. Thandayuthapani AP/ECE**



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**KINGS**  
COLLEGE OF ENGINEERING  
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Approved by AICTE, New Delhi  
Affiliated to Anna University, Chennai



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ONE DAY WORKSHOP ON

## **“Microelectronics and VLSI design using advanced simulation tools”**

### **POST WORKSHOP REPORT**

**Event** : Workshop  
**Title** : Microelectronics and VLSI design using advanced simulation tools  
**Date** : 20.02.2019  
**Number of Beneficiaries** : Internal participants: 56  
**Resource Persons** : Dr.T.N.Prabhakaran, Dean,  
Oxford College of Engineering,  
Tiruchirappalli  
**Venue** : VLSI LAB



*At Inauguration*

### OBJECTIVES OF ORGANIZING WORKSHOP:

- To meet curriculum requirements.
- To learn Hardware Descriptive Language (Verilog/VHDL).
- To learn the fundamental principles of VLSI circuit design in digital and analog domain



*Theory session*



*Validictroy function*

### OUTCOME:

- Students can able to write the Verilog/VHDL coding.
- Students can able to do the projects and create the new applications in VLSI domain.